



Attorney Docket No.: CYPR-CD00174

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**  
**Patent Application****RECEIVED**  
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Inventor(s): Monte Mar and Warren Snyder

Serial No.: 09/930,021

Group Art Unit:

Filed: 08/14/01

Examiner:

Title: PROGRAMMING METHODOLOGY AND ARCHITECTURE FOR A PROGRAMMABLE  
ANALOG SYSTEM (AS AMENDED)**Form 1449****U.S. Patent Documents**

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A	6,144,327	11/07/00	Distinti et al.	341	126	08/12/97
	B	5,202,687	04/13/93	Distinti	341	158	06/12/91
	C	6,166,367	12/26/00	Cho	250	208.1	03/26/99
	D	5,600,262	02/04/97	Kolze	326	38	10/11/95
	E	5,414,308	05/09/95	Lee et al.	327	293	07/29/92
	F	5,258,760	11/02/93	Moody et al.	341	166	07/13/92
	G	5,563,526	10/08/96	Hastings et al.	326	37	01/03/94
	H	6,225,866	05/01/01	Kubota et al.	330	295	06/14/00

**Foreign Patent or Published Foreign Patent Application**

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	I							
	J							

**Related Pending US Patent Applications**

Examiner Initial	No.	Docket Number, Title, Filing Date, Serial Number & Inventors
	K	CYPR-CD00173; "A PROGRAMMABLE ANALOG SYSTEM ARCHITECTURE (AS AMENDED)"; 07/18/01; 09/909,047; Mar
	L	CYPR-CD00175; "METHOD FOR SYNCHRONIZING AND RESETTING CLOCK SIGNALS SUPPLIED TO MULTIPLE PROGRAMMABLE ANALOG BLOCKS (AS AMENDED)"; 10/01/01; 09/969,311; B. Sullam
	M	CYPR-CD00194; "METHOD AND APPARATUS FOR LOCAL AND GLOBAL POWER MANAGEMENT IN A PROGRAMMABLE ANALOG CIRCUIT"; 08/22/01; 09/935,454; M. Mar
	N	CYPR-CD00232; "PROGRAMMABLE SYSTEM ON A CHIP"; 10/01/01; 10/033,027; W. Snyder
Examiner		Date Considered 4/13/04

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered.  
Include copy of this form with next communication to applicant.